

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicant: Cheung, et al.

Docket No: TI-32389.1

Serial No: 10/712,736

Examiner: Huynh, Kim

Filed: 11/12/2003

Art Unit: 2182

For: METHOD AND DEVICE FOR PROVIDING HIGH DATA RATE FOR A SERIAL PERIPHERAL INTERFACE

MAR 21 2005

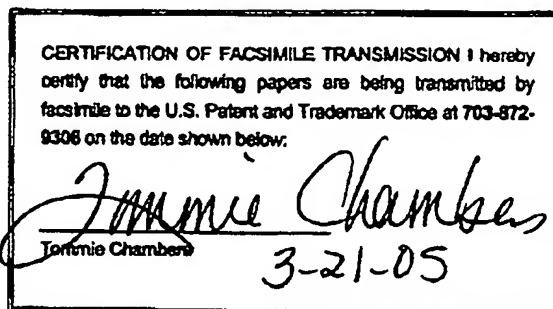
**NOTICE OF APPEAL FROM THE PRIMARY EXAMINER  
TO THE BOARD OF APPEALS**

Assistant Commissioner for Patents

PO Box 1450

Alexandria, VA 22313-1450

Dear Sir:



Applicant hereby appeals to the Board of Appeals from the decision mailed December 22, 2004, of the Primary Examiner finally rejecting Claims 7-11.

The item(s) checked below are appropriate:

1. ☐ An extension of time to respond to the final rejection  
☐ was granted on \_\_\_\_\_.  
☐ is requested for \_\_\_\_ month.
2. ☒ A timely response to the final rejection has been filed, as provided in 841 O.G. 1411.
3. ☒ Fee \$500.00:  
☐ Not required (Fee paid in prior appeal)  
☒ The Commissioner of Patents is hereby authorized to charge any fees which may be required or credit any overpayment to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.  
This form is submitted in duplicate.

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